

U.S. Department of Commerce, Patent and Trademark Office					Atty Docket No.	Serial No.	
					40532-P001M11P3	10/044,785	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)					Applicant(s)		
					Peter D. Geiger, et al.		
					Filing Date	Group	
					1/11/02	2819	
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*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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	C3	M. Kjelso et al.; "Performance evaluation of computer architectures with main memory data compression"; Journal of Systems Architecture; Elsevier Science Publishers Bv.; Amsterdam, NL; Vol. 45, No. 8; February 1999 (1999-02); pgs. 571-590; X0004153056; ISSN: 1383-7621; pg. 574, left-hand column, paragraph 4; pg. 580, left-hand column, paragraph 2.
Examiner	Date Considered	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.		

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